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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)					
Sheet	1	of	1	Attorney Docket No.	TI-36818

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
8M	AA	6,493,251	B2	Hoya et al.	12/10/2002	Entire Document
8M	AB	2003/0103372	A1	Matsushita	06/05/2003	Entire Document
8M	AC	2003/0174532	A1	Matsushita et al.	09/18/2003	Entire Document
8M	AD	2003/0206430	A1	Ho	11/06/2003	Entire Document
8M	AF	6,667,896	B2	Rickes et al.	12/23/2003	Entire Document
	AG	X				

FOREIGN PATENT DOCUMENTS

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	CB	X	
	CC	X	
	CD	X	
	CE	X	
	CF	X	
	CG	X	
	CH	X	

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known Application Number 10/805,809 Filing Date March 22, 2004 First Named Inventor Madan et al. Group Art Unit 2827 Examiner Name S. MAI Attorney Docket No. TI-36818	
Sheet	1	of	1		

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
JM	AA	6,046,928		Takata	04/04/2000	Entire Document
JM	AB	6,208,550	B1	Kim	03/27/2001	Entire Document
JM	AC	6,487,103	B2	Yamamoto et al.	11/26/2002	Entire Document
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JM	AG	10/717,146		Sudhir K. Madan	11/18/2003	Entire Document
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JM	AI	10/847,412		Summerfelt et al.	05/17/2004	Entire Document
JM	AJ	10/866,834		Madan et al.	06/14/2004	Entire Document

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JM	CA	"A Bit-Line GND Sense Technique for Low-Voltage Operation FeRAM", SHOICHIRO KAWASHIMA, TORU ENDO, AKIRA YAMAMOTO, KEN'ICHI NAKABAYASHI, MITSUHARU NAKAZAWA, KEIZO MORITA and MASAKI AOKI, 2001 Symposium on VLSI Circuits Digest of Technical Papers, pgs. 127 & 128.			
JM	CB	"A Survey of Circuit Innovations in Ferroelectric Random-Access Memories", ALI SHEIKHOLESLAMI and P. GLENN GULAK, Proceedings of the IEEE, Volume 88, No. 5, May 2000, pp. 667-689.			
	CC	X			
	CD	X			
	CE	X			

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